

Fig. 1

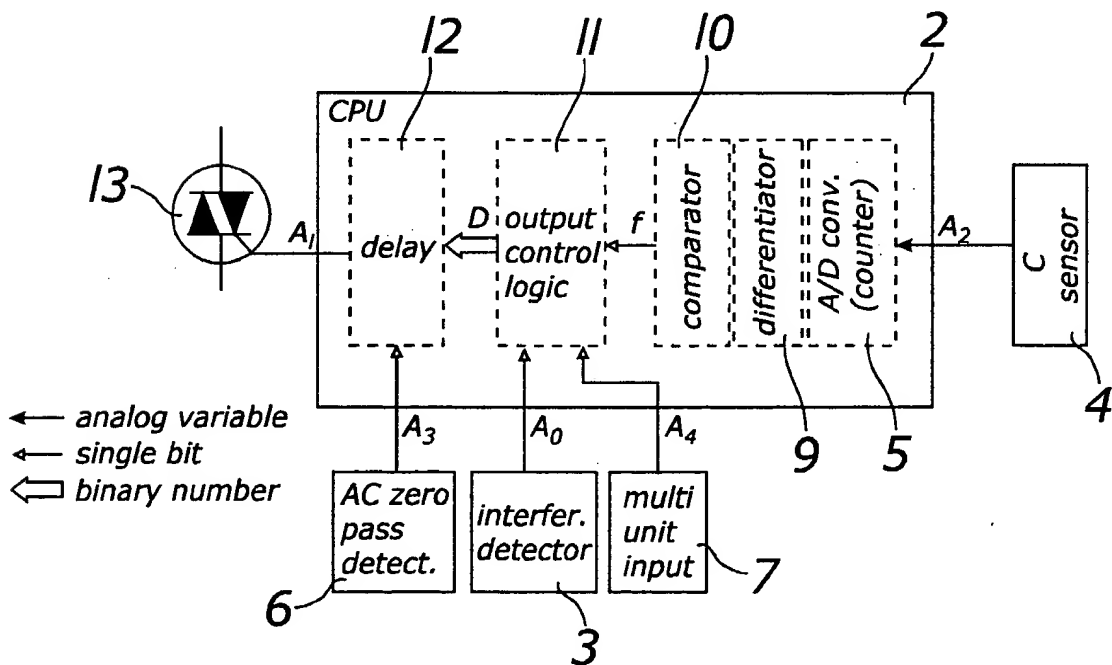


Fig. 2a

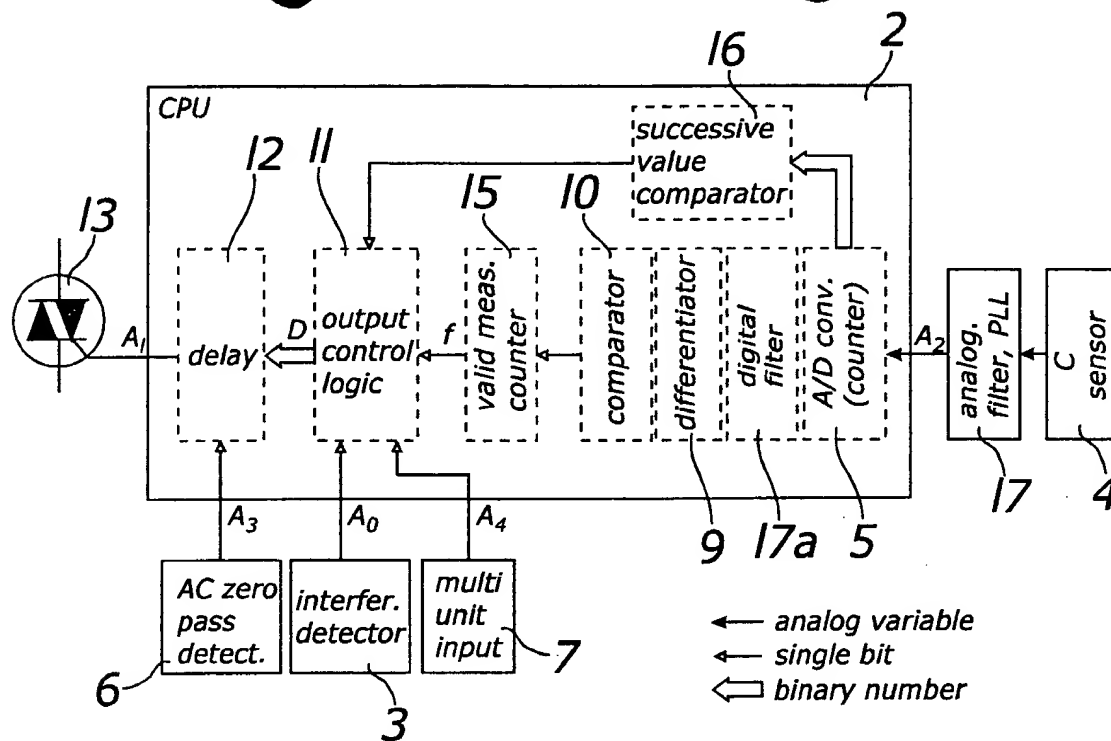


Fig. 2b

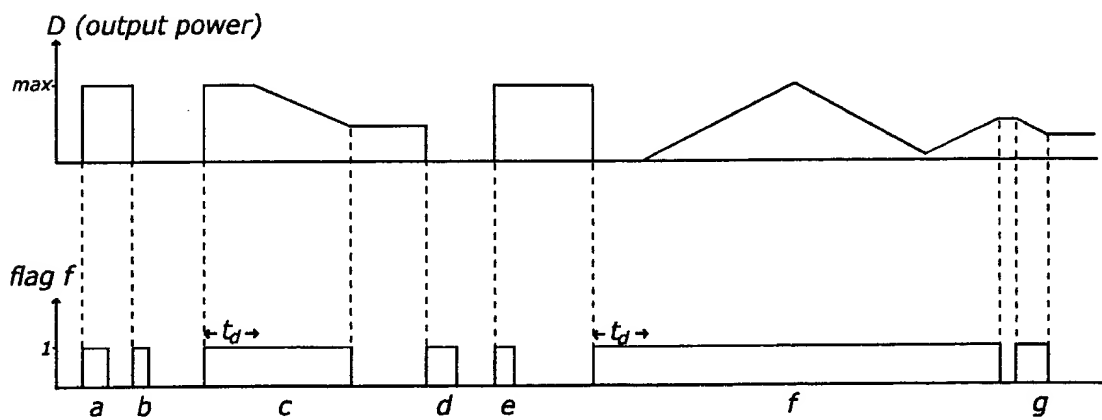
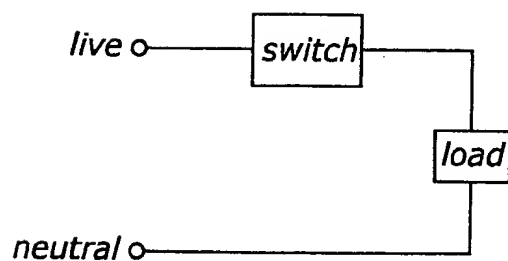


Fig. 3

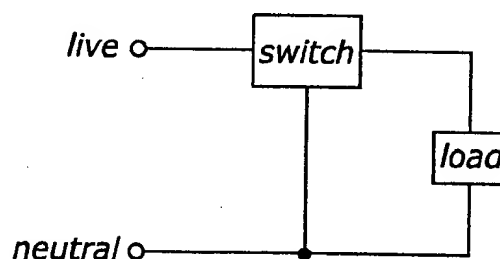
← analog variable
 - - - single bit
 □ binary number

Fig. 4



connection in series

Fig. 7a



parallel connection

Fig. 7b

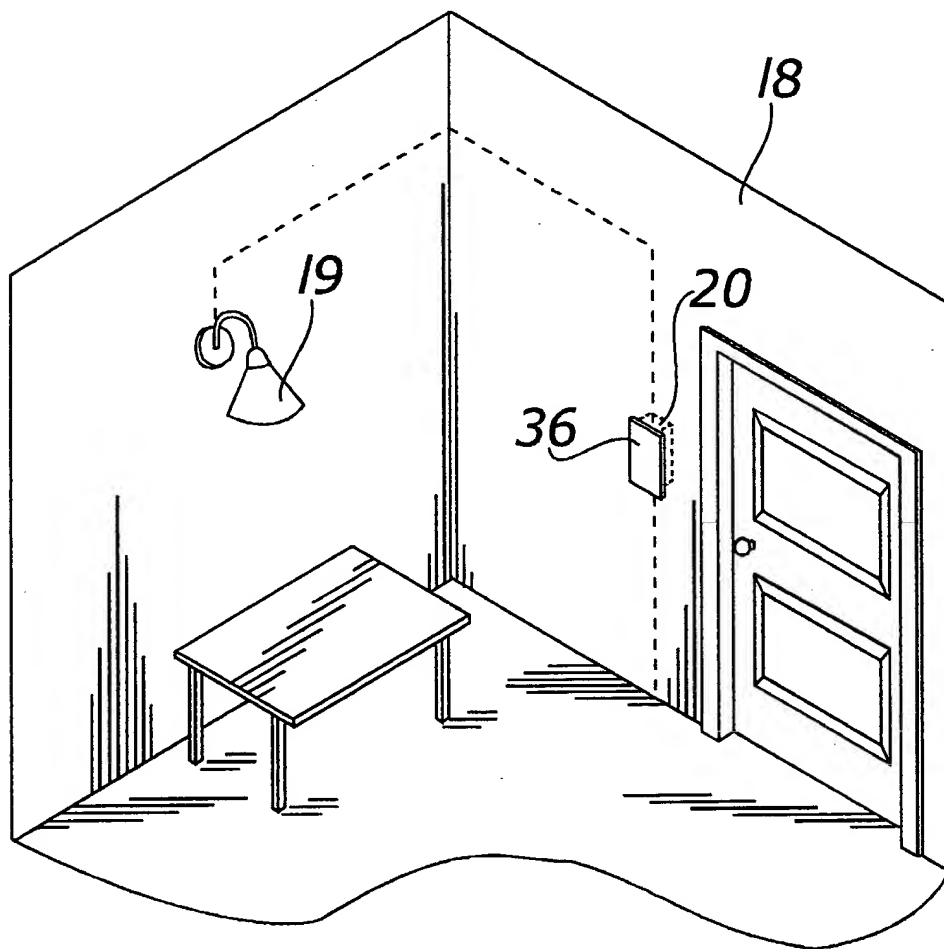


Fig. 5

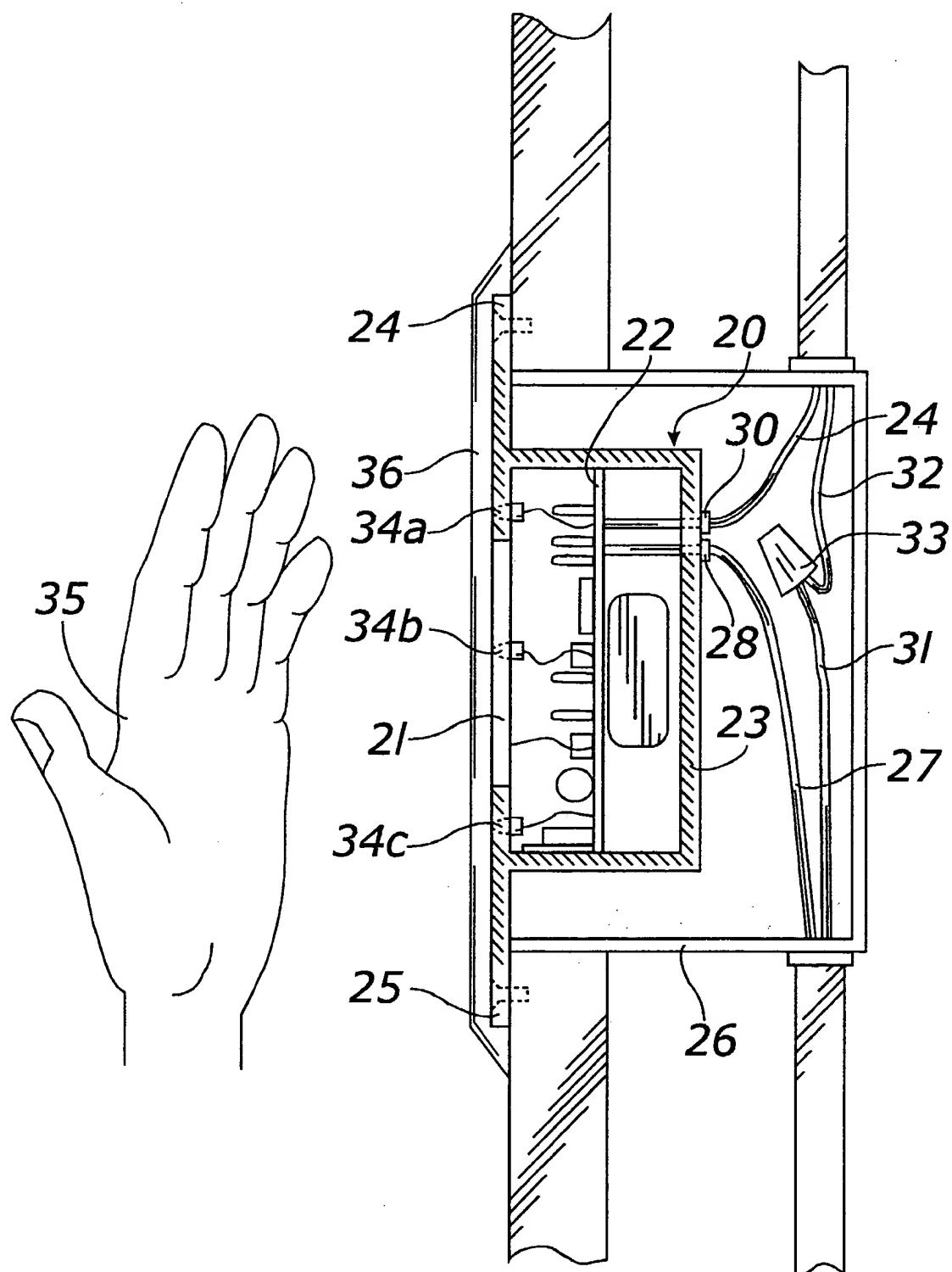


Fig. 6

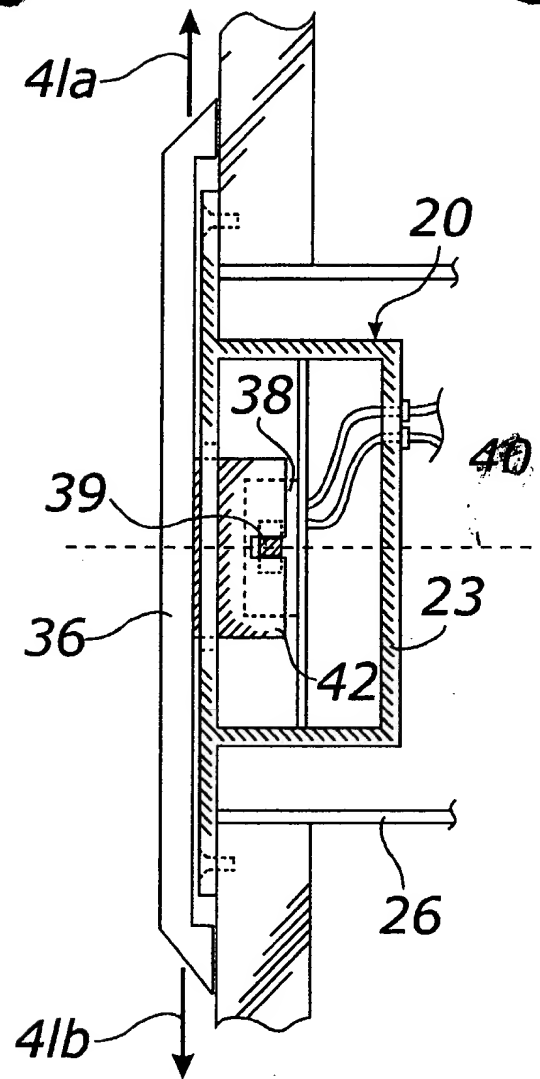


Fig. 8a

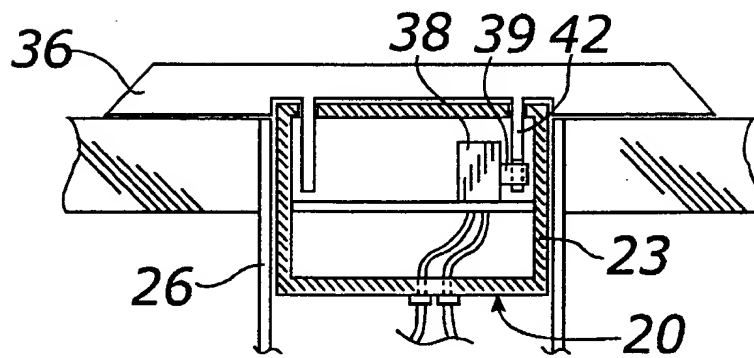


Fig. 8b

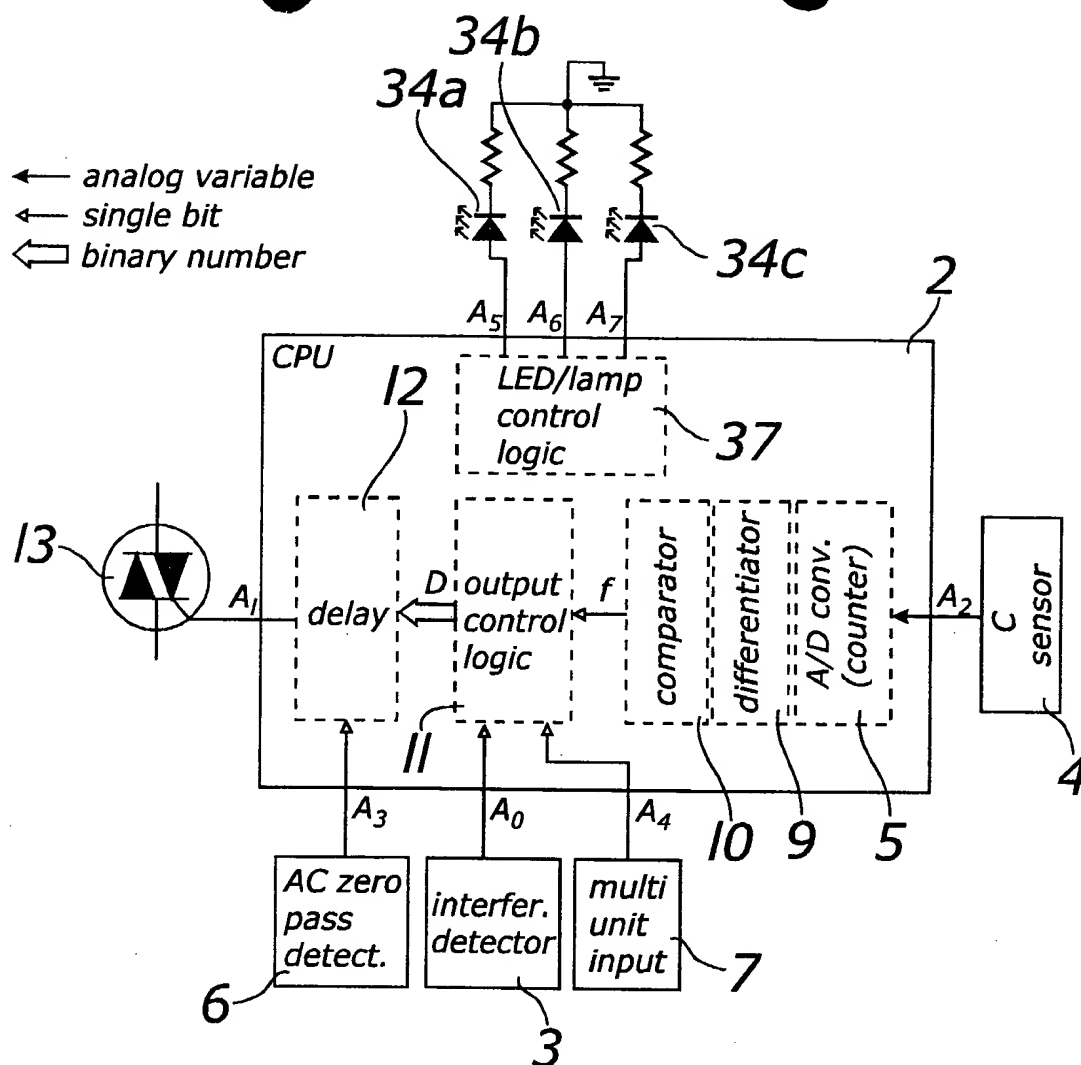


Fig. 9a



Fig. 9b



Fig. 9c

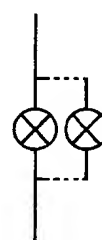


Fig. 9d